

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	1640	(load near balancing) and routing and packets and (processors or engines or controllers)	US- PGPUB; USPAT	2005/10/26 09:05	
2	BRS	L2	1166	1 and input and output	US- PGPUB; USPAT	2005/10/26 09:06	
3	BRS	L3	1088	2 and (memory or que)	US- PGPUB; USPAT	2005/10/26 09:06	
4	BRS	L4	49	3 and (packet near processors)	US- PGPUB; USPAT	2005/10/26 09:08	